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The diagram illustrates a digital optical disk system with the following components and connections:

- 101 OPTICAL DISK**: The data source, connected to the **102 OPTICAL HEAD**.
- 102 OPTICAL HEAD**: Receives data from the disk and sends it to the **103 PREAMPLIFIER**.
- 103 PREAMPLIFIER**: Outputs to the **104 AGC CIRCUIT** and the **106 BINARIZATION CIRCUIT**.
- 104 AGC CIRCUIT**: Outputs to the **105 WAVEFORM EQUALIZATION CIRCUIT**.
- 105 WAVEFORM EQUALIZATION CIRCUIT**: Outputs to the **106 BINARIZATION CIRCUIT**.
- 106 BINARIZATION CIRCUIT**: Outputs to the **107 DATA PLL CIRCUIT**.
- 107 DATA PLL CIRCUIT**: Receives **READ DATA** and **DATA CLOCK** signals. It outputs to the **108 DATA DEMODULATION CIRCUIT** and the **109 CURRENT SETTING CIRCUIT**.
- 108 DATA DEMODULATION CIRCUIT**: Receives **READ DATA** and **DATA CLOCK** signals.
- 109 CURRENT SETTING CIRCUIT**: Receives signals from the **107 DATA PLL CIRCUIT** and the **111 N DIVIDER**. It outputs to the **113 PHASE COMPARATOR**.
- 110 MOTOR**: Connected to the **102 OPTICAL HEAD** and the **118 SYSTEM CONTROLLER**.
- 111 N DIVIDER**: Receives **FIXED CLOCK** and outputs to the **113 PHASE COMPARATOR** and the **112 M DIVIDER**.
- 112 M DIVIDER**: Receives **M SET VALUE** and outputs to the **113 PHASE COMPARATOR**.
- 113 PHASE COMPARATOR**: Receives signals from the **109 CURRENT SETTING CIRCUIT** and the **112 M DIVIDER**. It outputs to the **114 CHARGE PUMP**.
- 114 CHARGE PUMP**: Outputs to the **115 LPF**.
- 115 LPF**: Outputs to the **116 VCO**.
- 116 VCO**: Receives **REFERENCE CLOCK** and outputs to the **107 DATA PLL CIRCUIT** and the **109 CURRENT SETTING CIRCUIT**.
- 118 SYSTEM CONTROLLER**: Receives signals from the **110 MOTOR** and the **116 VCO**, and outputs to the **112 M DIVIDER**.

FIG. 2

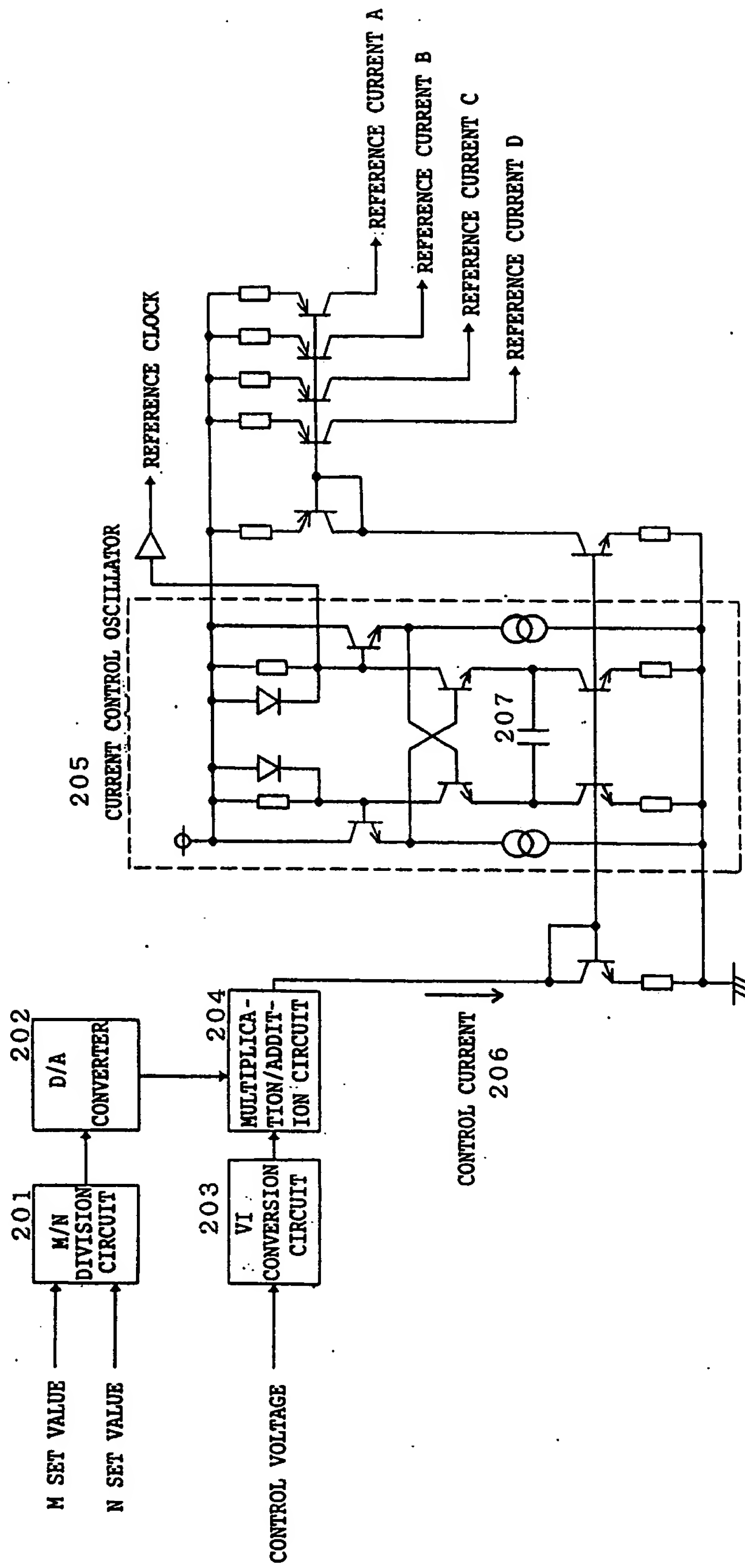


FIG. 3

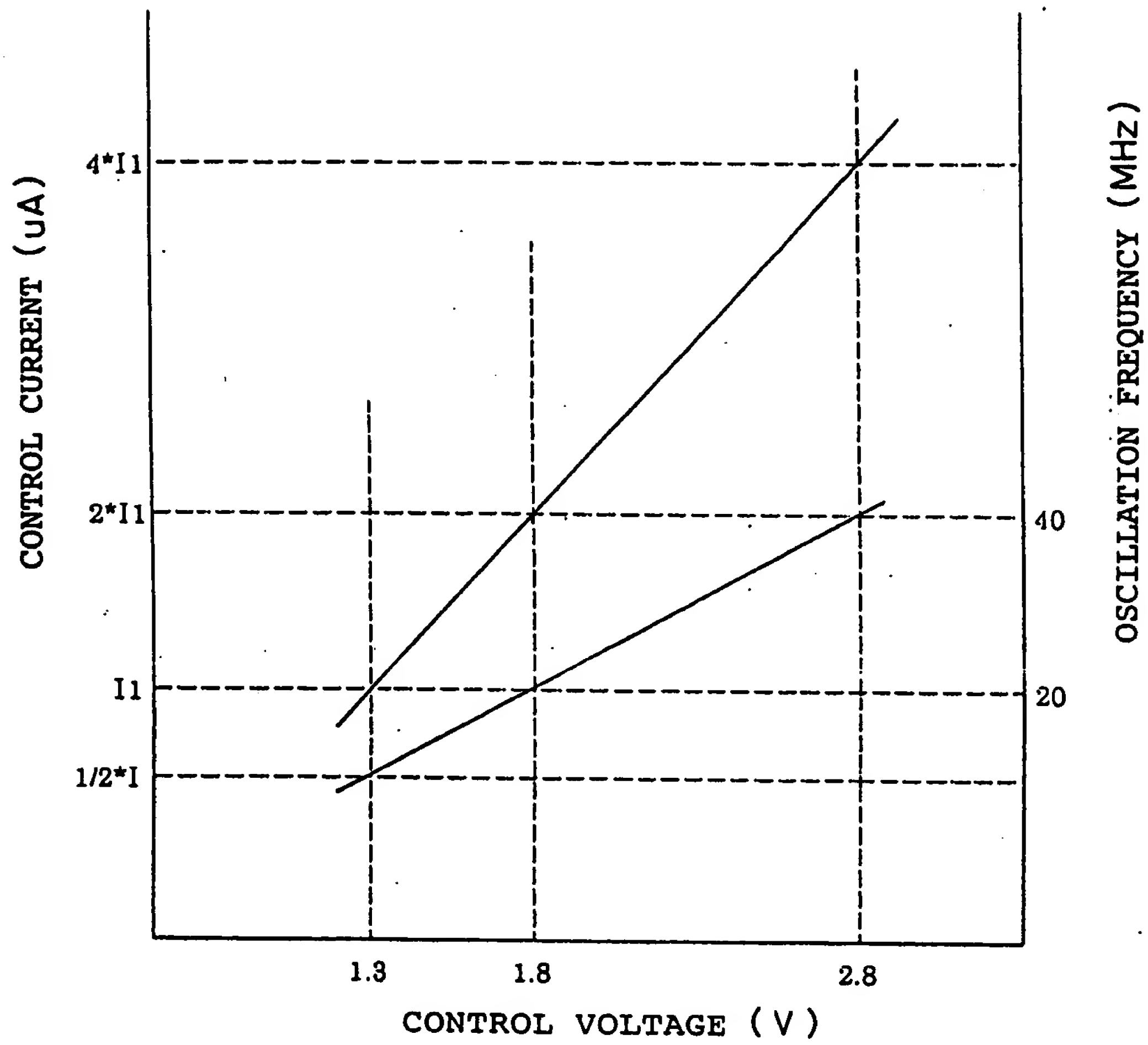


FIG. 4

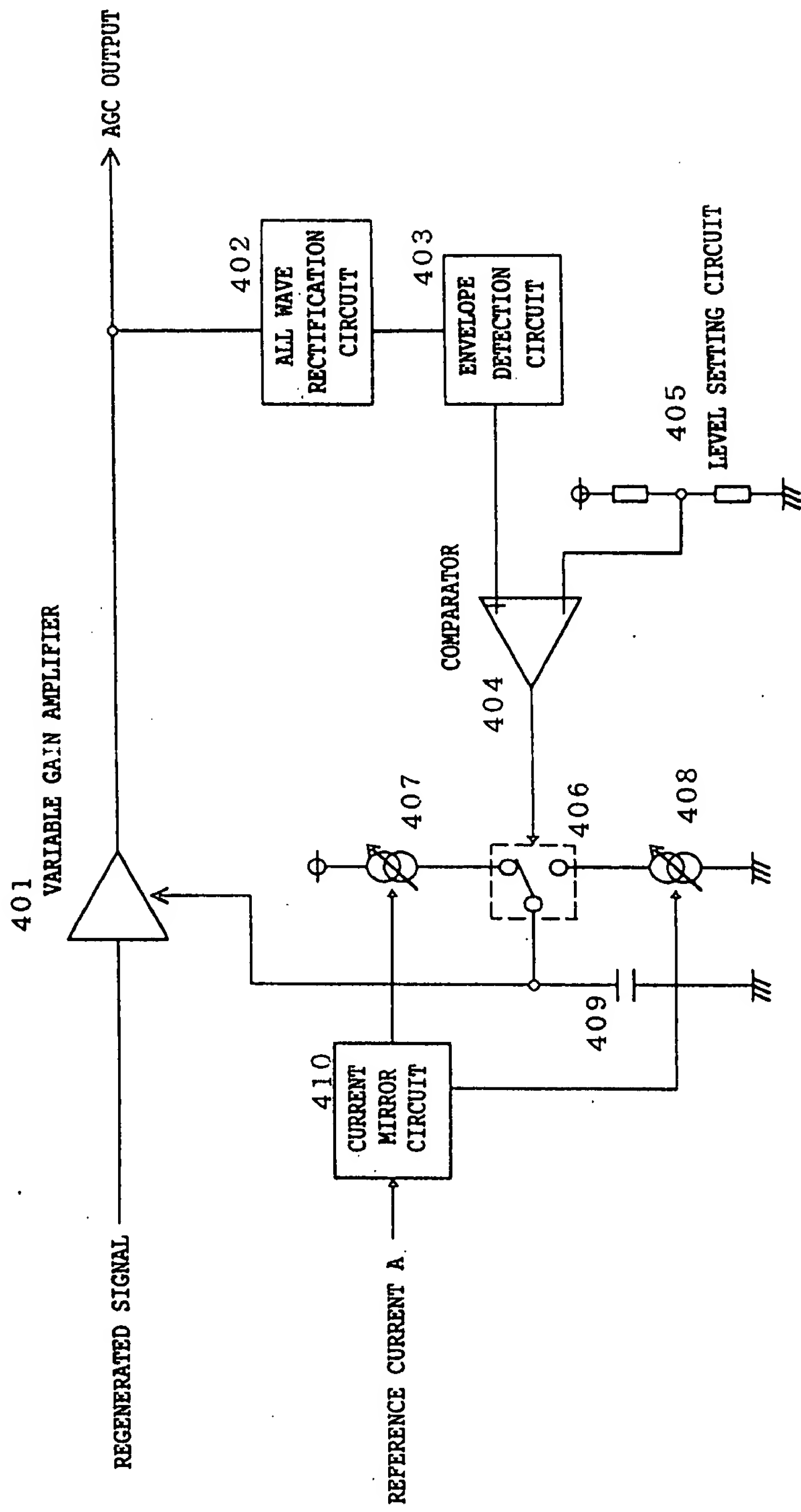


FIG. 5

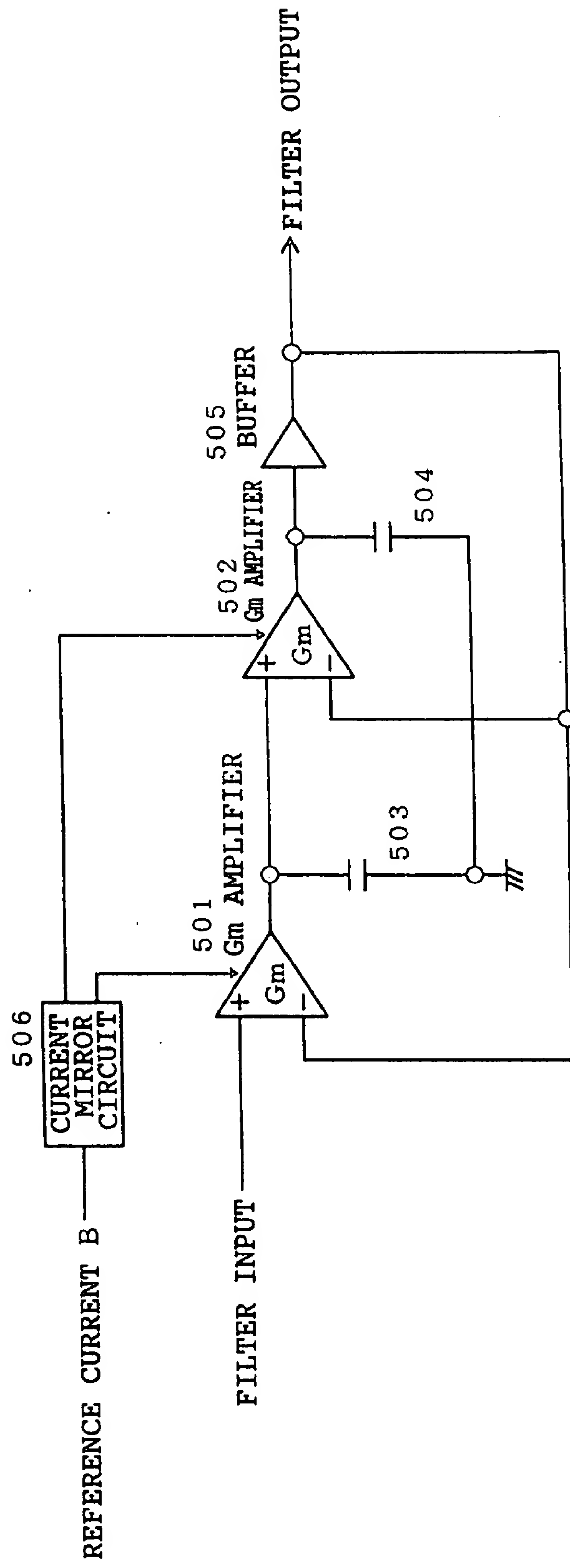


FIG. 6

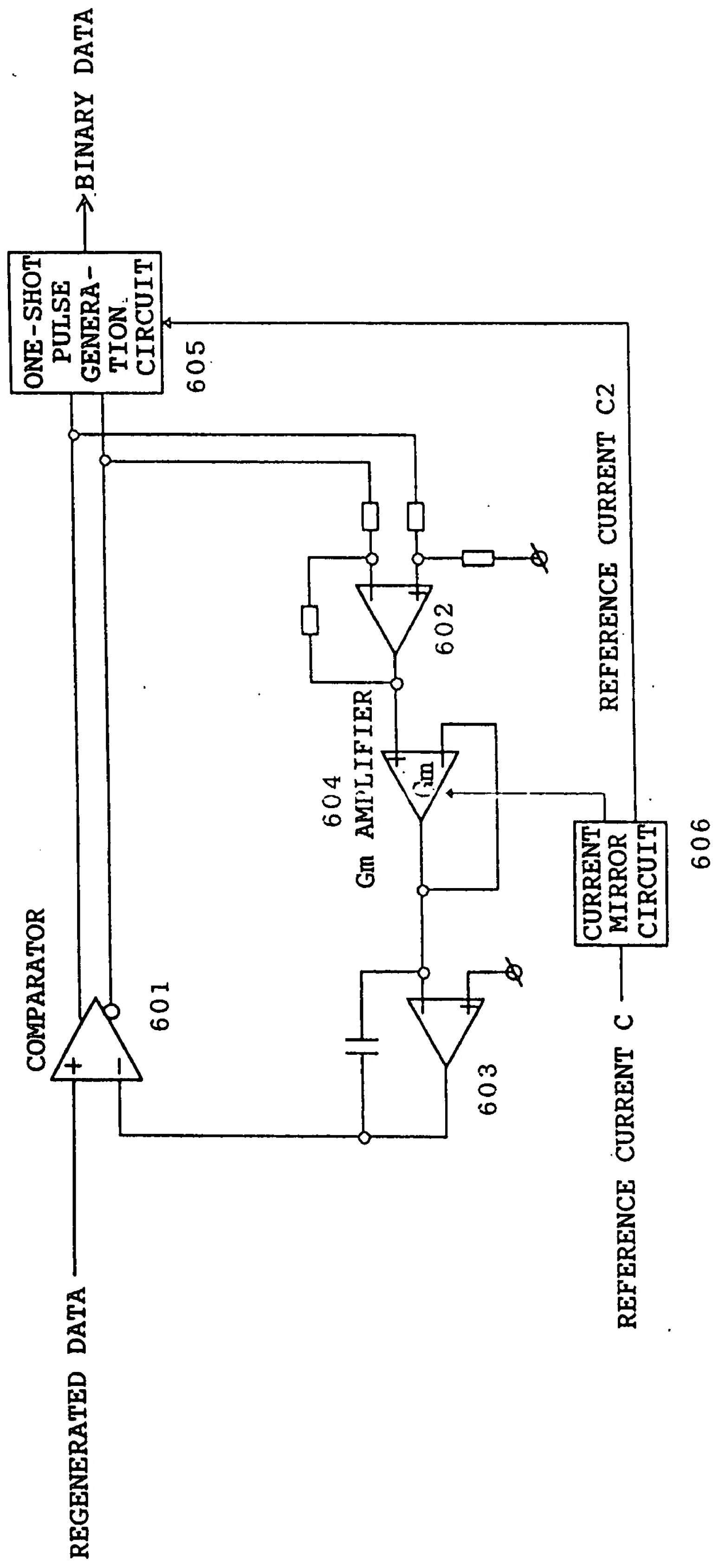


FIG. 7

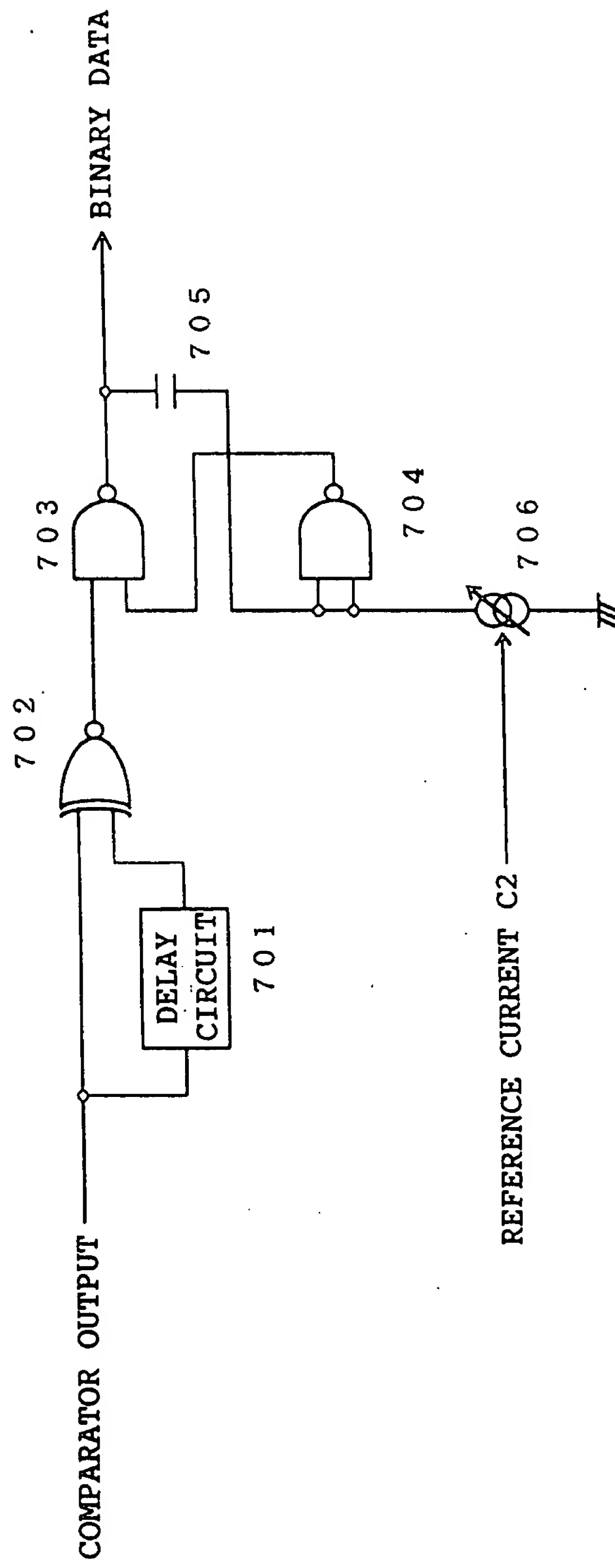


FIG. 8

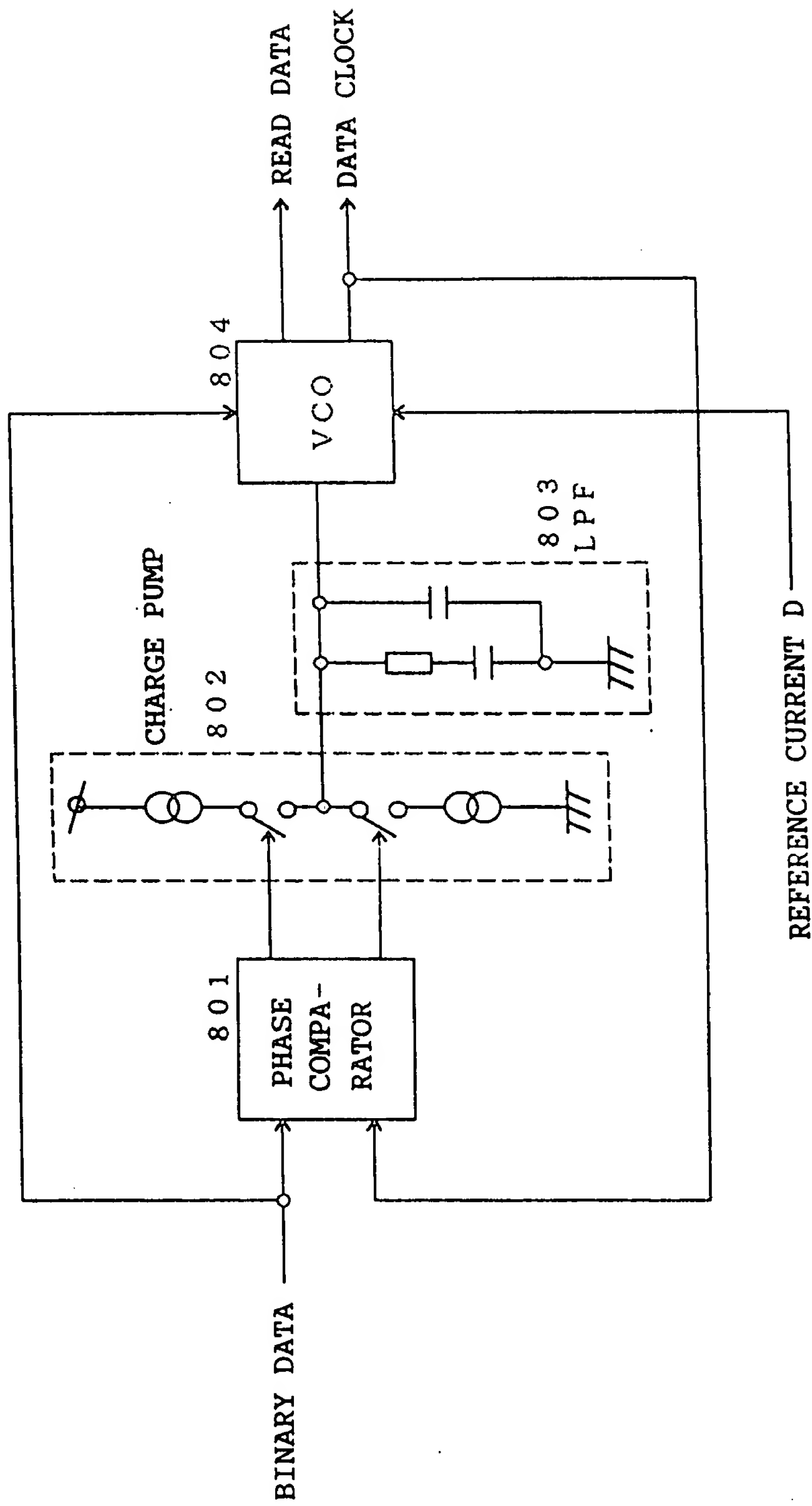


FIG. 10

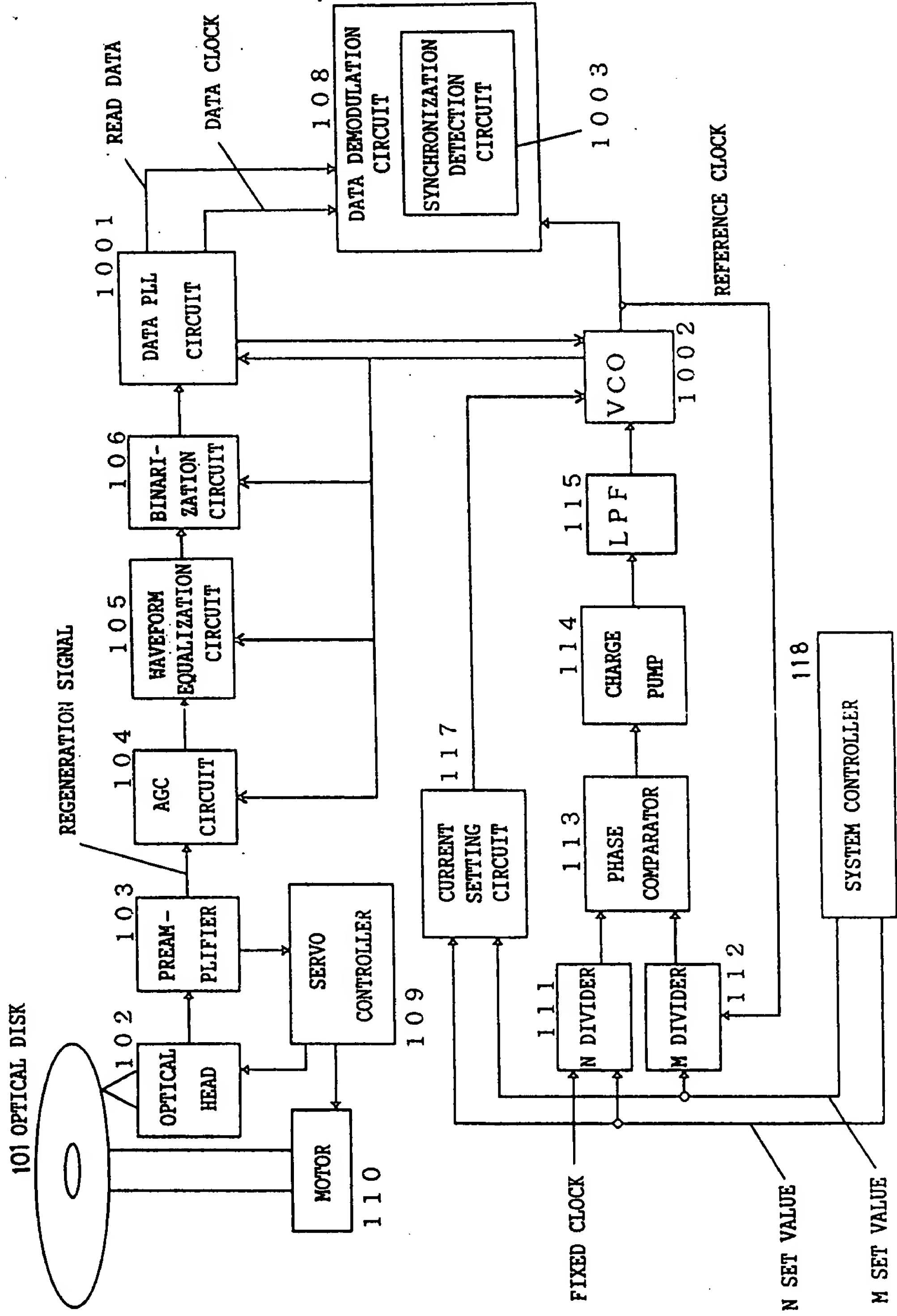


FIG. 11

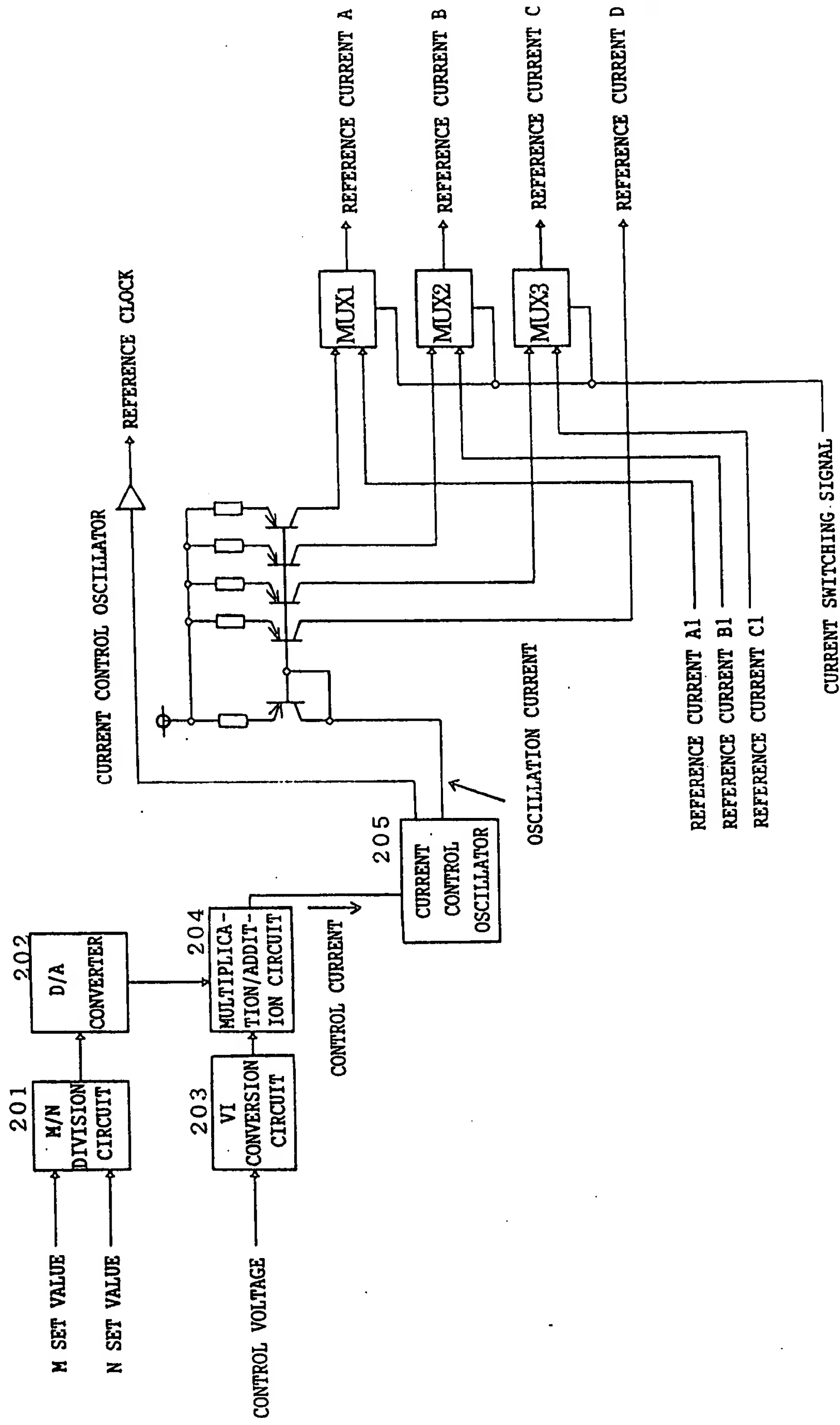


FIG. 12

